

100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189 190 191 192 193 194 195 196 197 198 199 200

100

101

CENTRAL  
PROCESSING  
UNIT

102

DIRECT  
MEMORY  
ACCESS  
UNIT

MEMORY  
UNIT

ASYNCHRONOUS  
TRANSFER  
MODE  
INTERFACE  
UNIT

103

HIGHEST  
PRIORITY  
ENTERPRISE  
UNIT

104

0

0

0

0

10N

110

120

EXTERNAL  
PROCESSING  
UNIT

Fig. 1

TI-33534

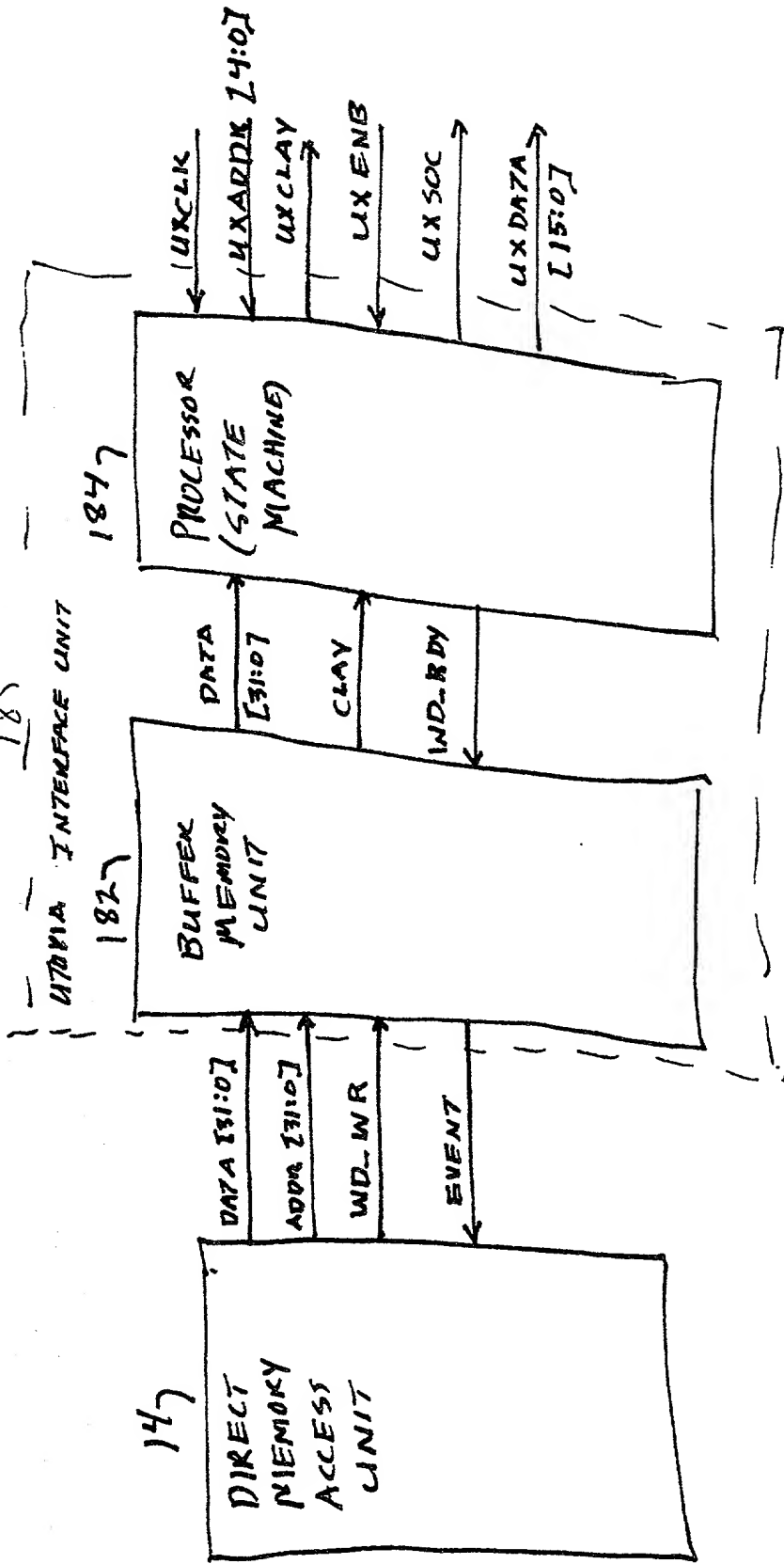


Fig. 2

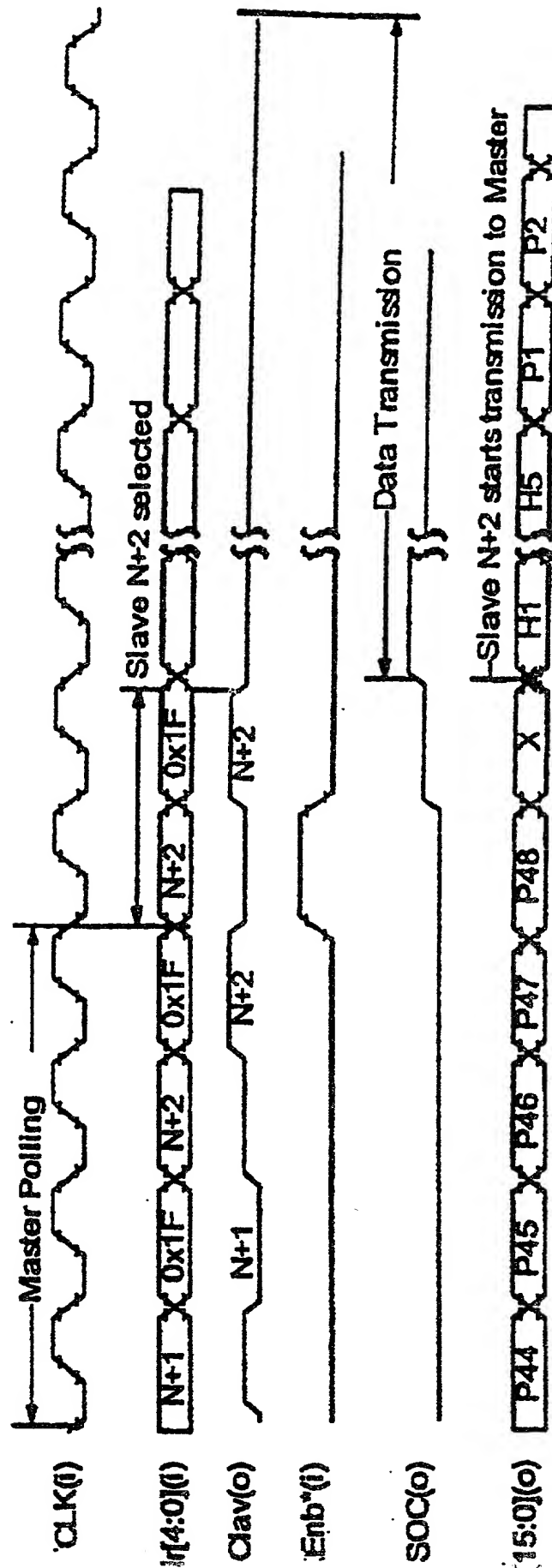


Fig. 3

14)

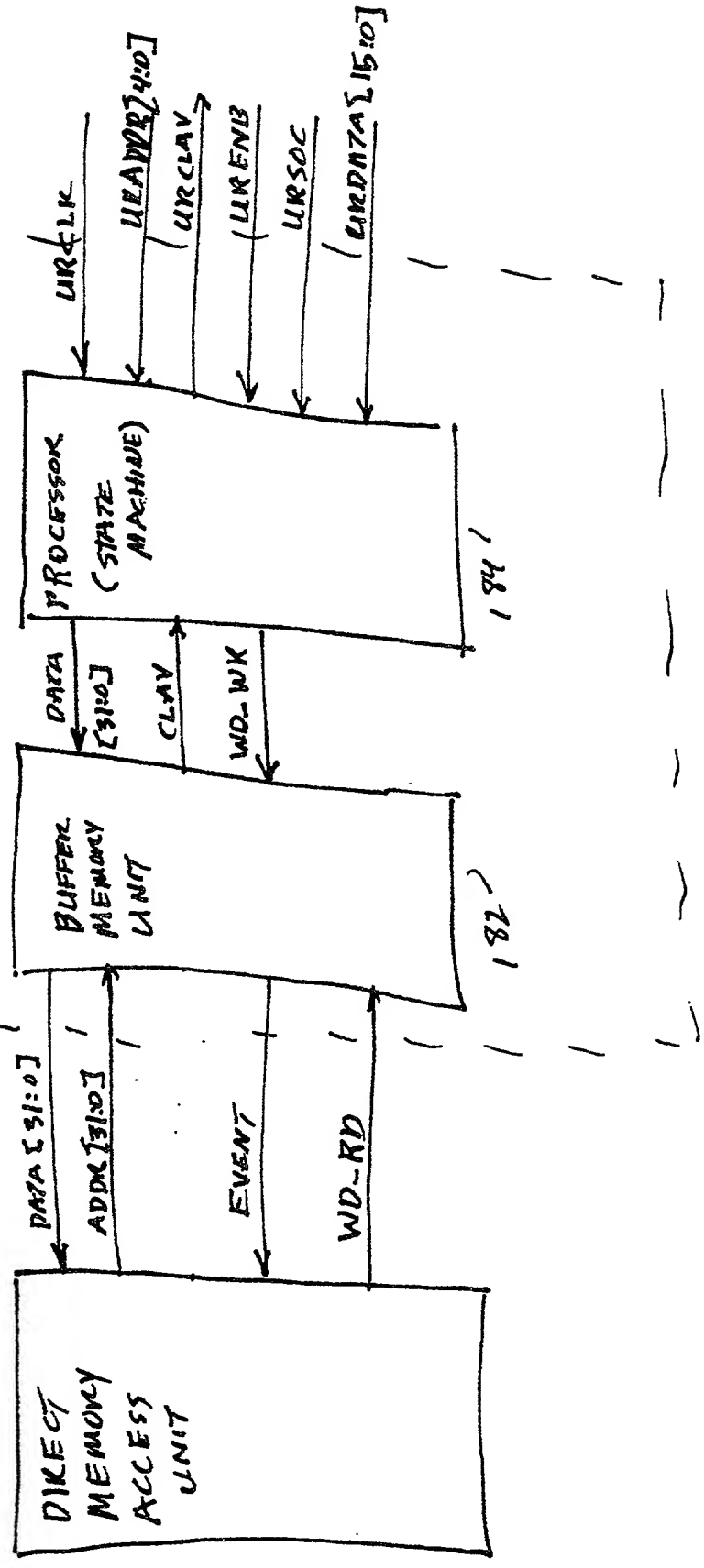


Fig. 4

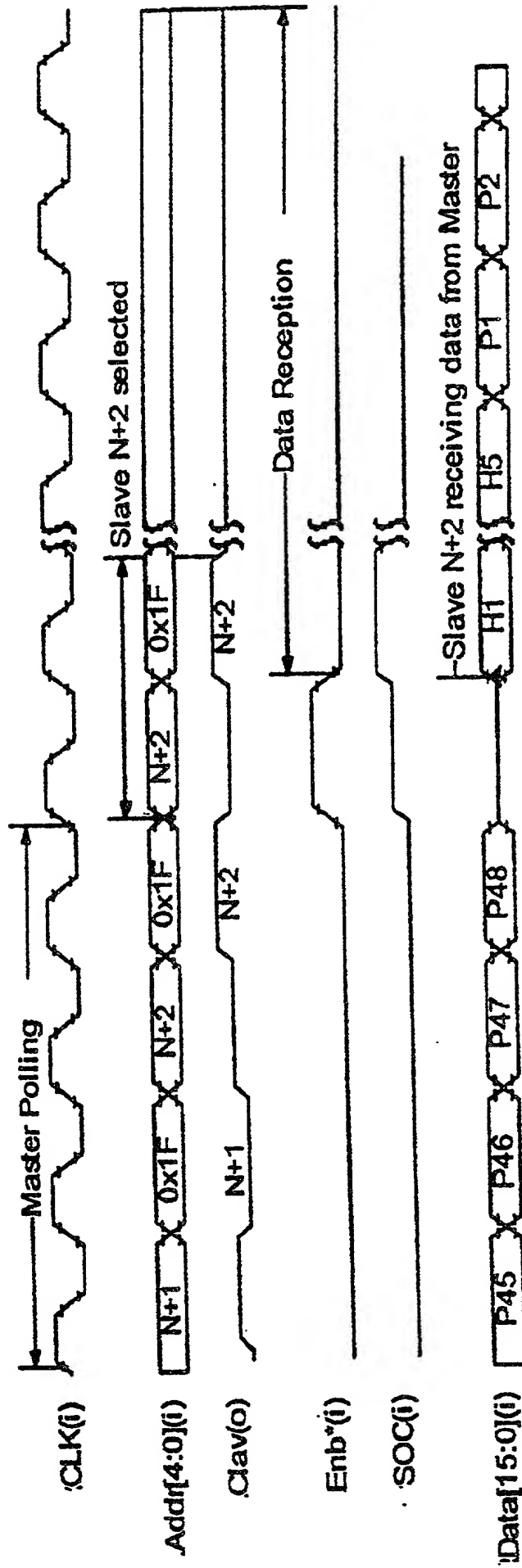


Fig. 5

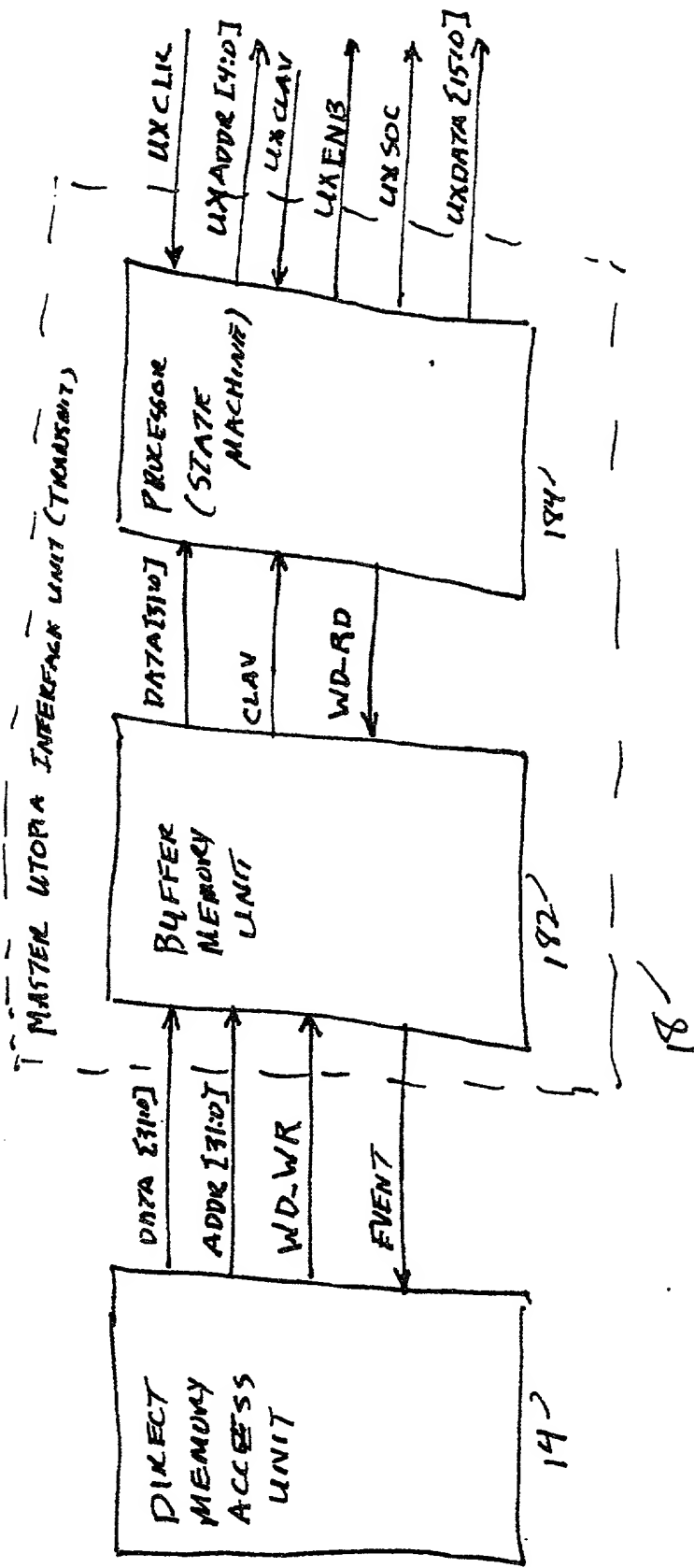


Fig. 6

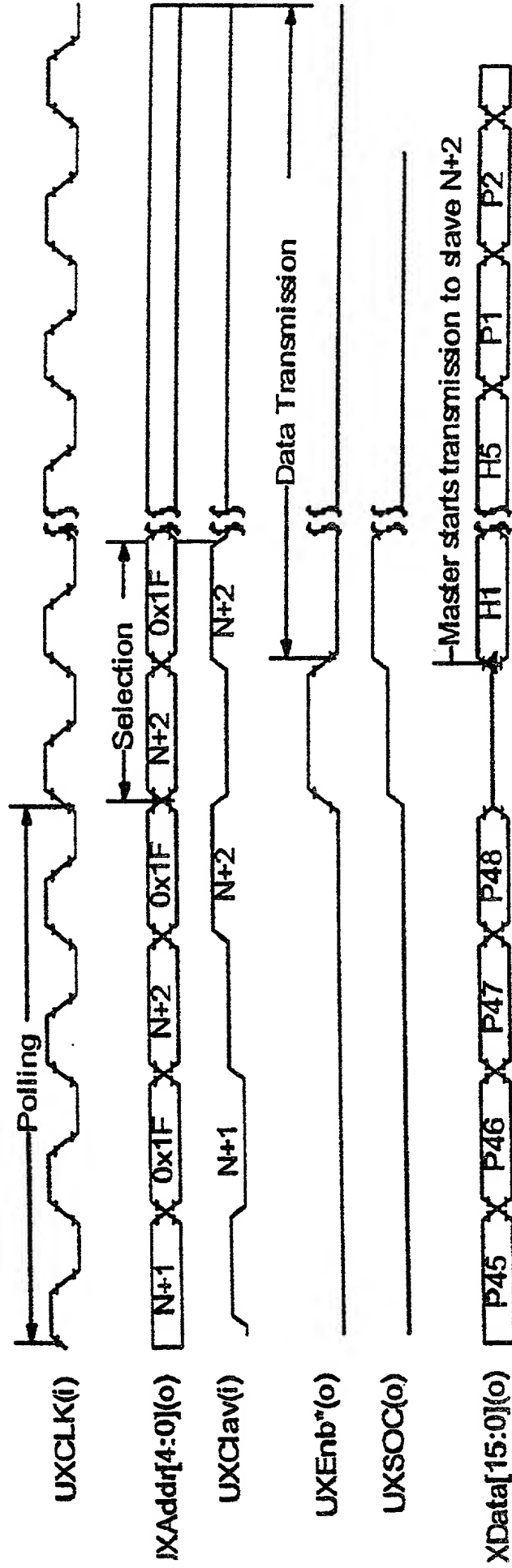


Fig. 17

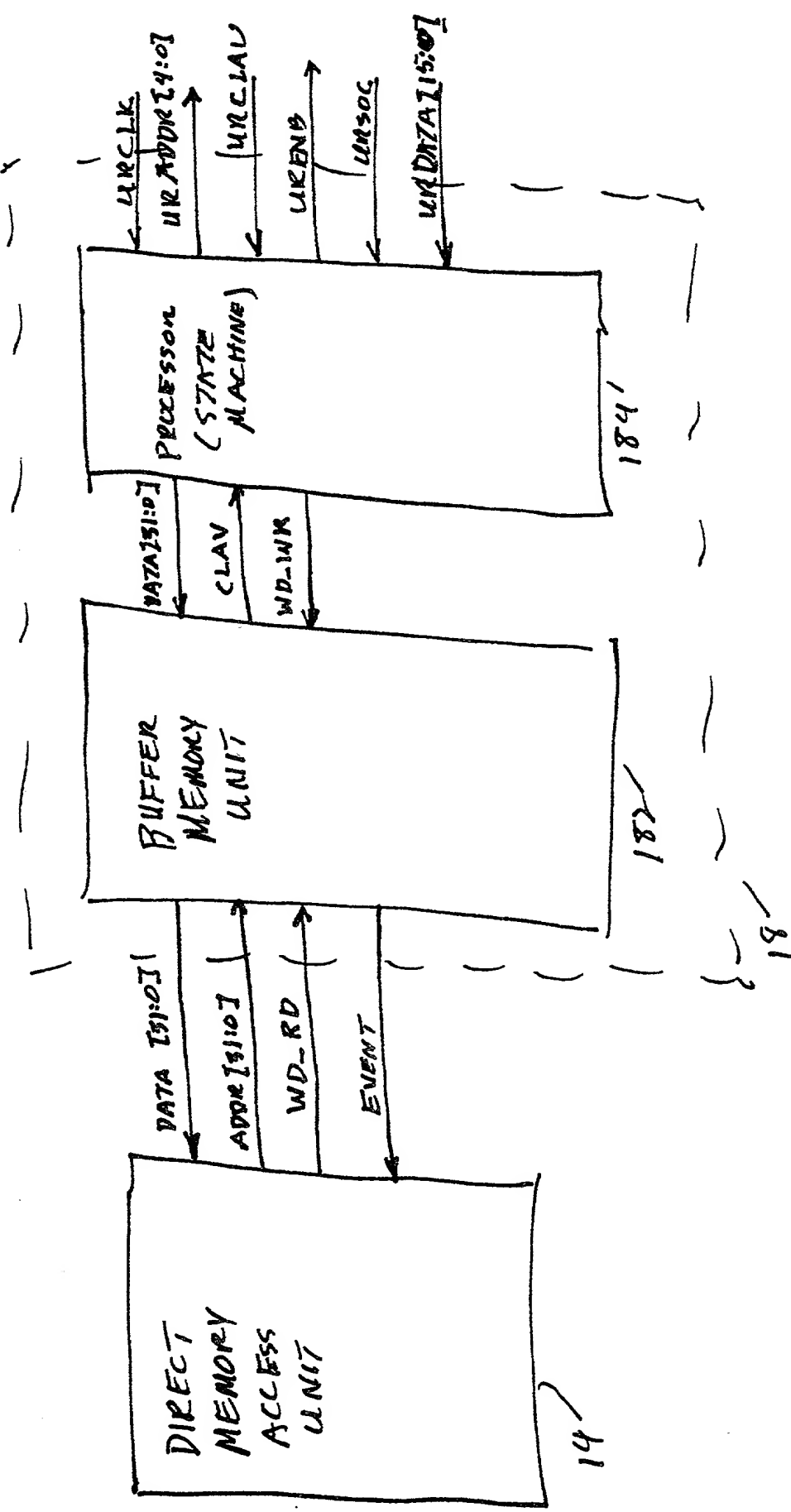


Fig. 8



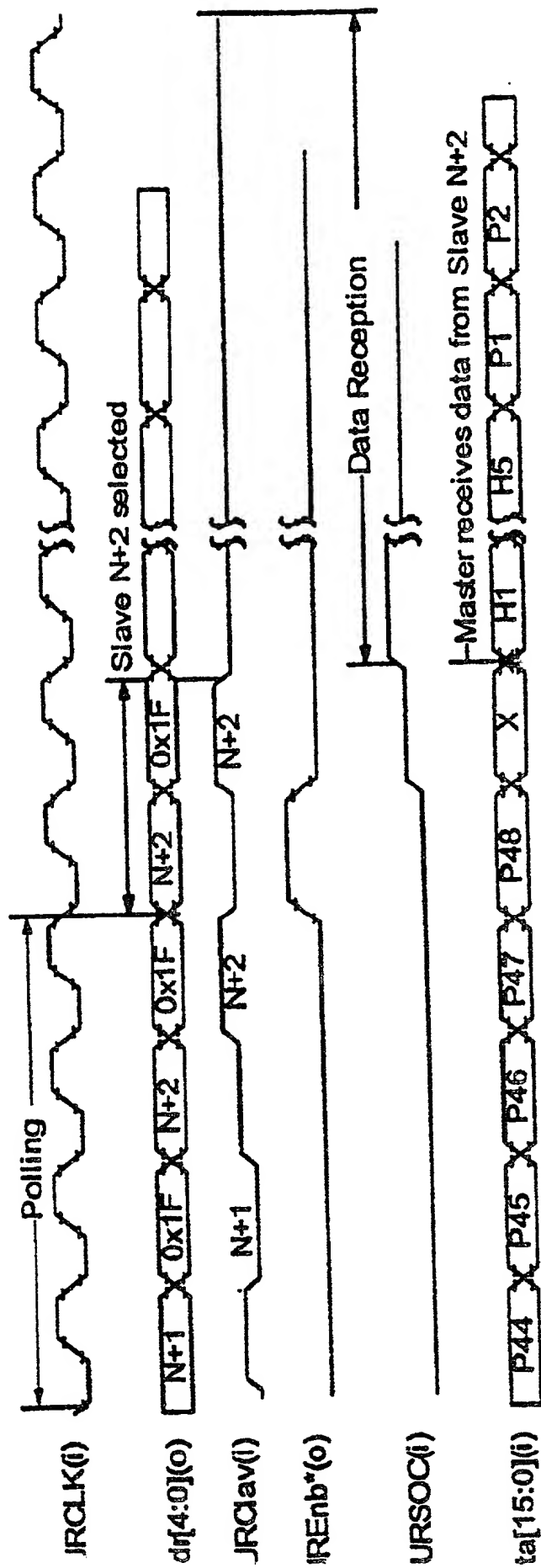


Fig. 9

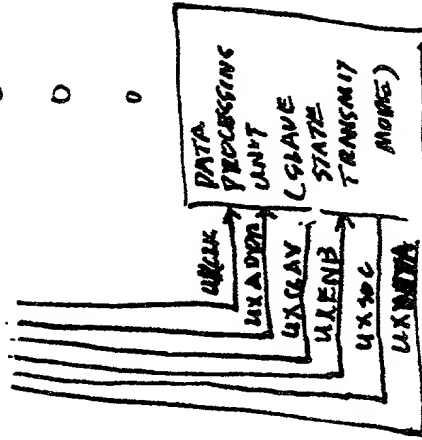
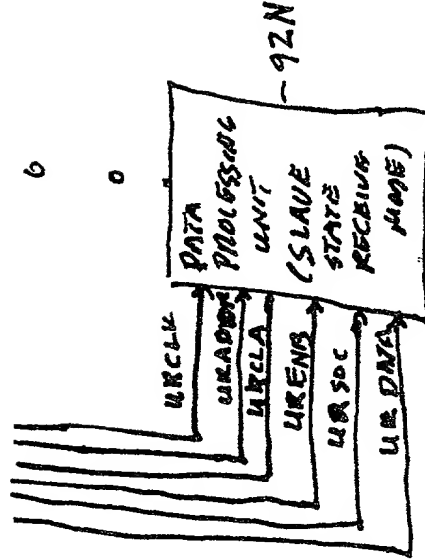
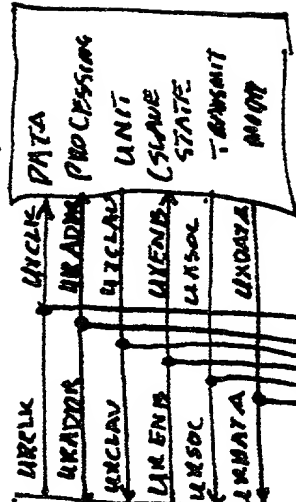
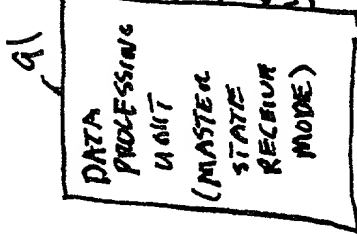
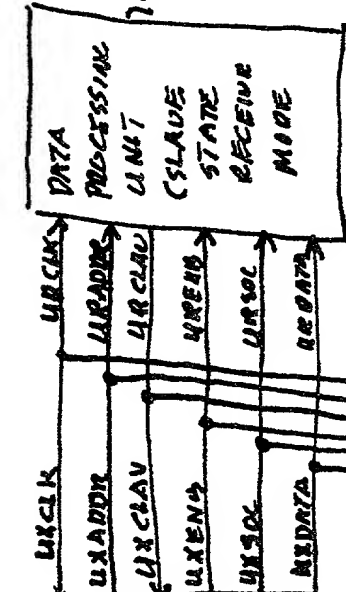
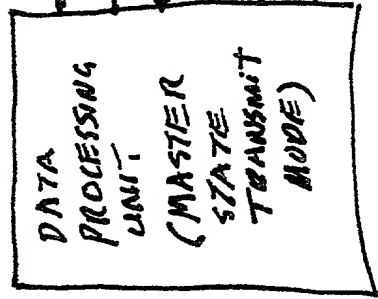


Fig. 10A

Fig. 10B

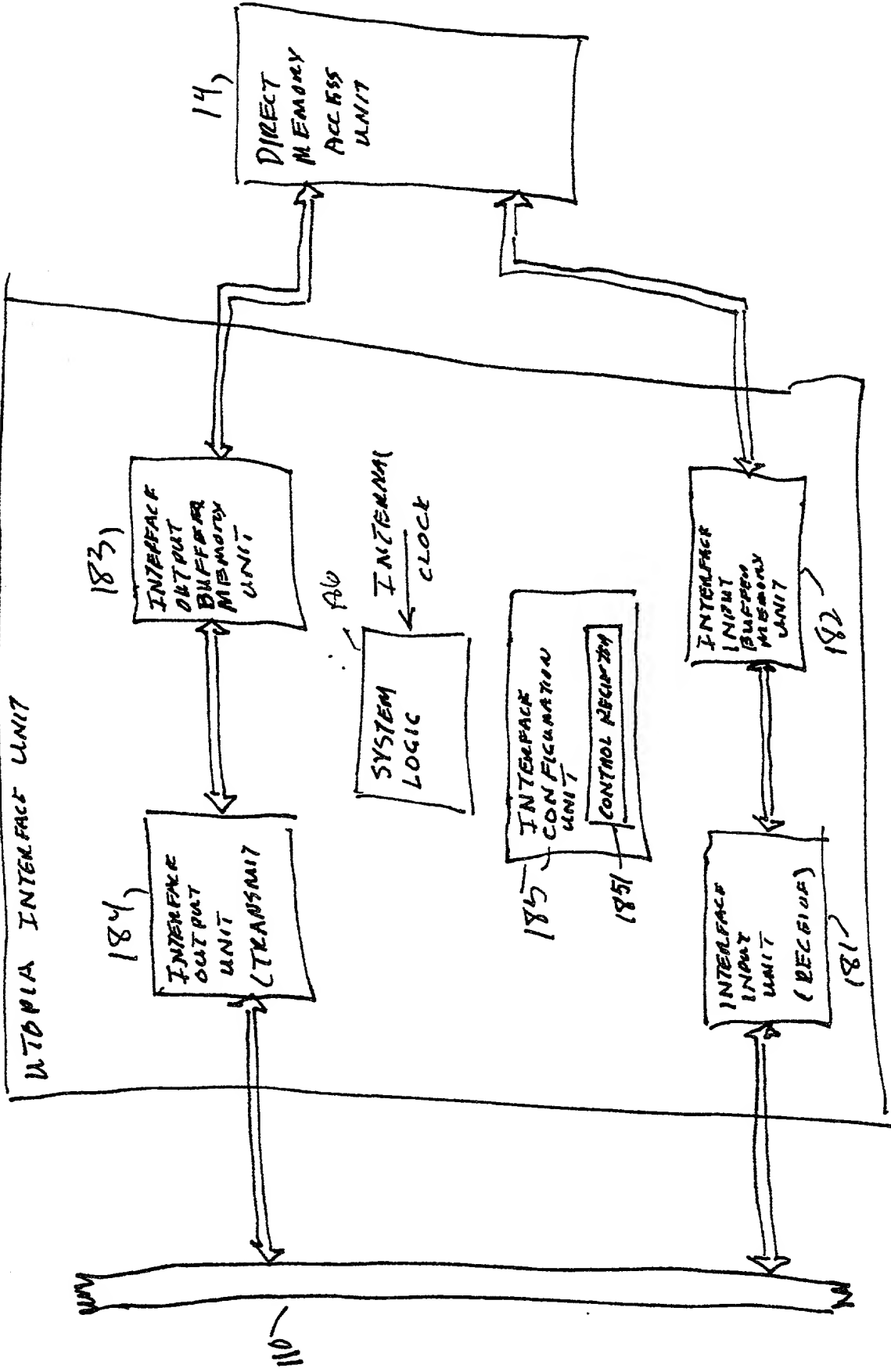


Fig. 11

1	30	29	28:24		23:22		21:18		17		16
JD	Rsv	Rsv	SLID / SLEND		Rsv	XUDC	UXMSTR	UXEN			
2	14	13	12	11:10	9:8	7:6	5:2	1	0		
3	B	MPHY	U16M	UPM	Rsv		Rsv	RUDC <td>URMSTR</td> <td>UREN</td>	URMSTR	UREN	

Fig. 12

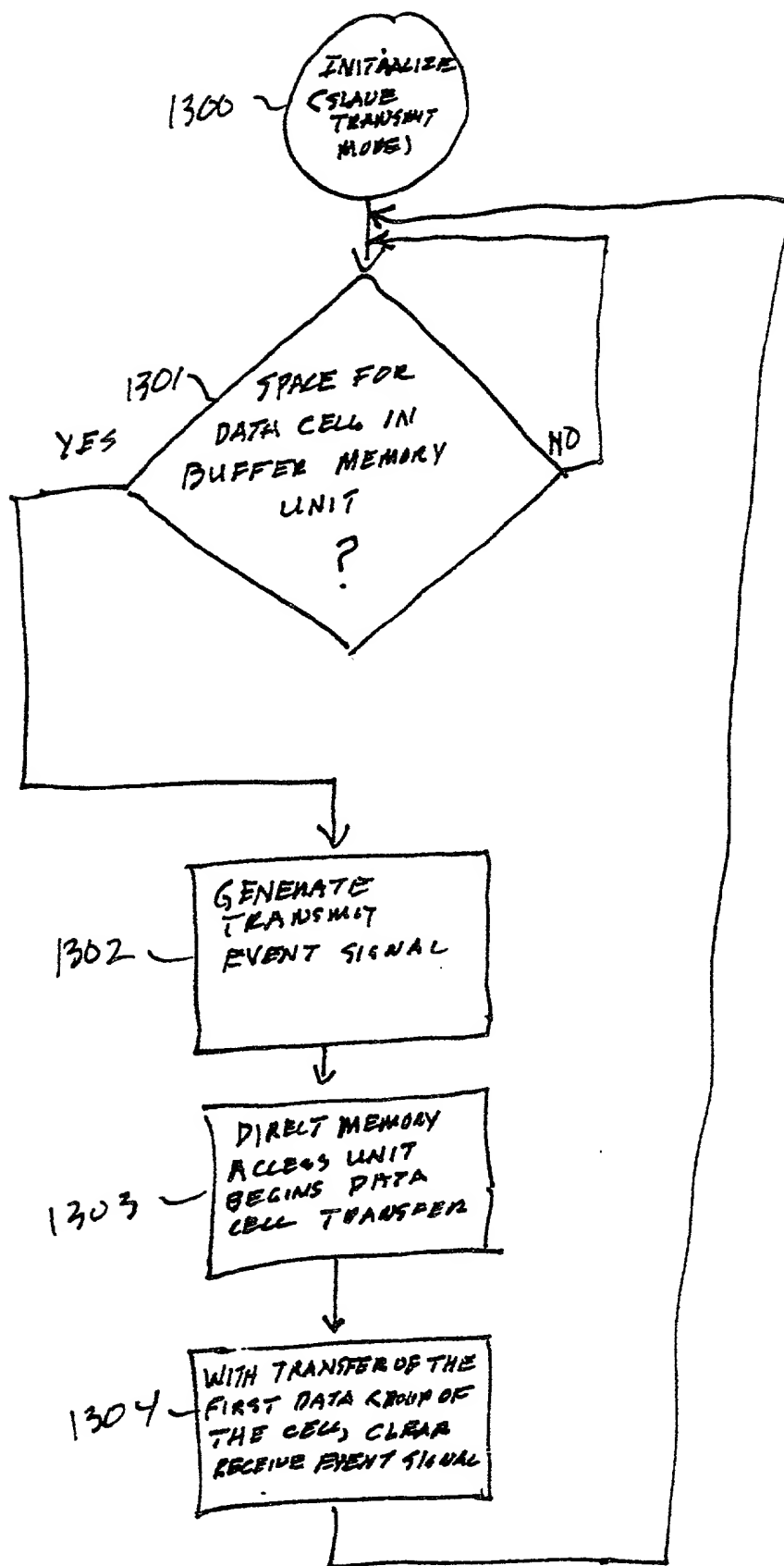


Fig. 13

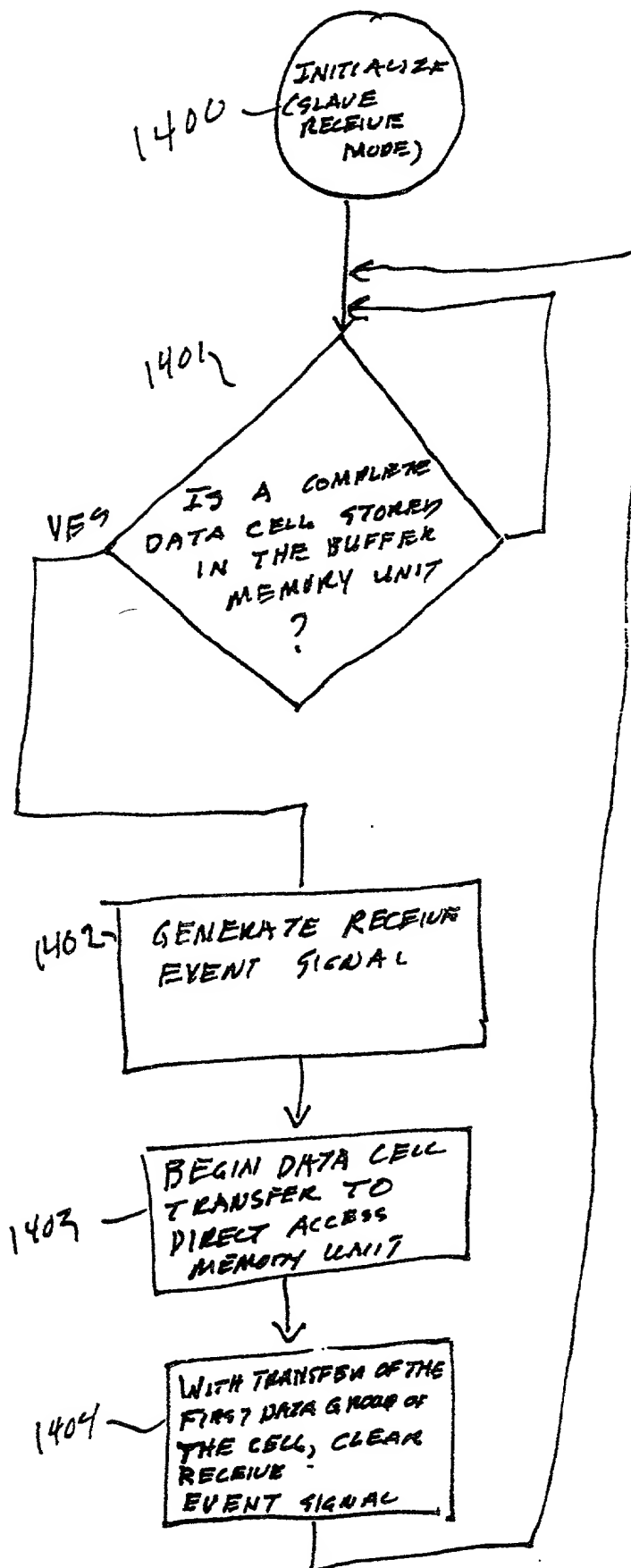


Fig. 14

(18)

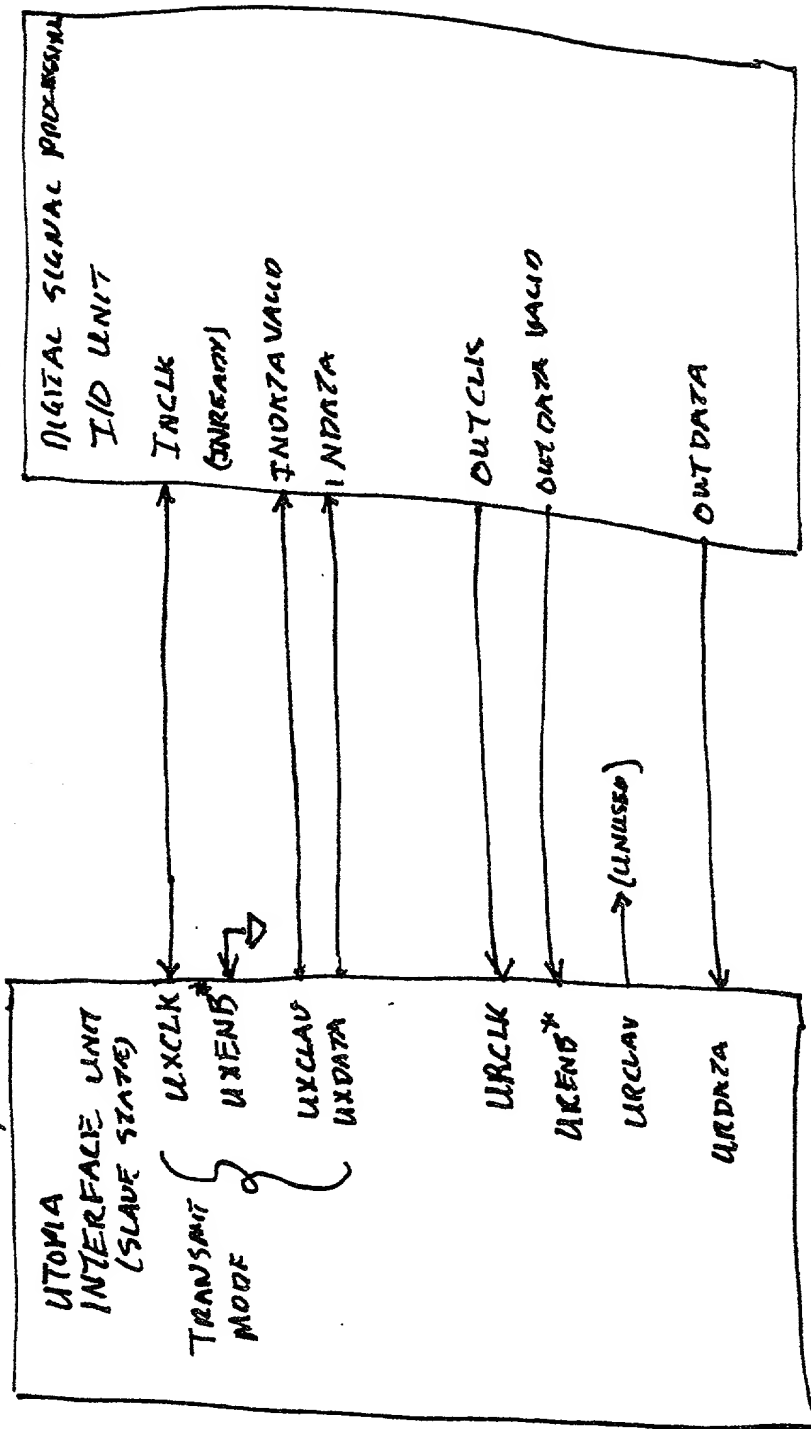


Fig. 15

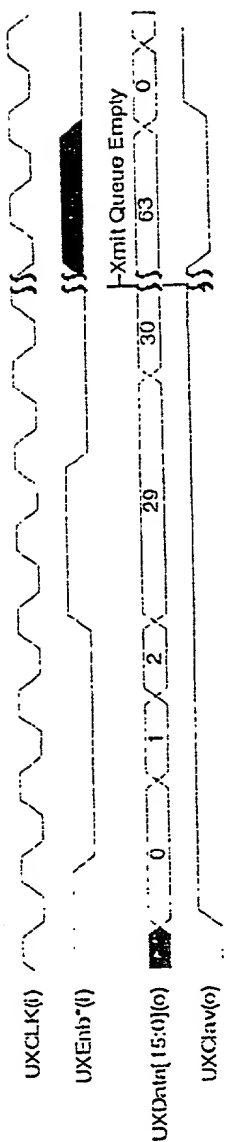


Fig. 16A

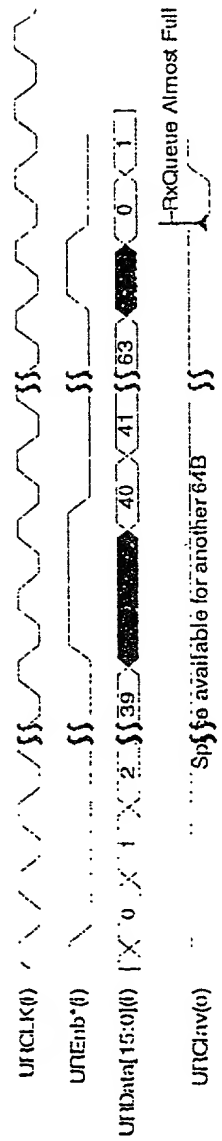


Fig. 16B